



PATENT

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Signature

Applicant : Amleshi Peerouz, et al.
Application No. : 10/722,204
Filed : November 25, 2003
Title : METHODS FOR FABRICATING AN ALIGNED OPTOELECTRONIC
WAVEGUIDE CIRCUIT

Confirmation No. 6811

Date of Notice
of Allowance : June 6, 2005
Grp./Div. : 2874
Examiner : Phan T.H. Palmer
Docket No. : 53531/SAH/E349

TRANSMITTAL OF DRAWINGS

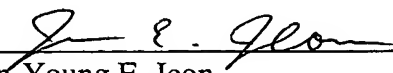
Mail Stop Issue Fee
Commissioner for Patents
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Post Office Box 7068
Pasadena, CA 91109-7068
August 5, 2005

Commissioner:

Enclosed are 3 sheets of formal drawings for this application. The formal drawings were originally submitted on August 1, 2005, but are now being submitted with payment of Issue Fee for the Examiner's convenience.

Respectfully submitted,
CHRISTIE, PARKER & HALE, LLP

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